



Donald Bailey is currently Professor of Imaging Systems at Massey University, and is co-director of the Centre for Research in Image and Signal Processing. Donald has spent over 35 years applying image processing technology to a range of industrial, machine vision and robot vision applications. For the last 18 years one area of particular research focus has been exploring aspects using FPGAs for implementing and accelerating image processing algorithms. He is the author of many publications in this field, including the book “Design for Embedded Image Processing on FPGAs”, published by Wiley / IEEE Press. He is a Senior Member of the IEEE, and is active in the New Zealand Central Section. He is currently chair of the IEEE New Zealand Council.